Please delete the paragraph on page 2, lines 5-9, and insert the following in place thereof:

The present application is a continuation of U.S. Patent Application Serial No. 09/598,934 filed June 22, 2000, now issued as U.S. Patent No. 6,724,814, which in turn claims priority from Provisional U.S. Patent Application Serial No. ~~60/140,075~~ 60/140,705 filed June 24, 1999 and Provisional U.S. Patent Application Serial No. ~~(UNASSIGNED, DOCKET NO CRUS-0156)~~ 60/140,825 filed on June 26, 1999~~, both of which are incorporated herein by reference~~.

Please delete the paragraphs on page 2, lines 13-22 and page 3, lines 1-3, and insert the following in place thereof:

~~Attorney docket No: 0931~~ U.S. Patent Application Serial No. 09/592,539 entitled "Digital Impairment Learning Sequence" filed on June 12, 2000, now issued as U.S. Patent No. 6,301,296 on October 9, 2001, which is incorporated herein by reference;

~~Attorney docket No: 0933~~ U.S. Patent Application Serial No. 09/598,680 entitled "Inter-Modulation Distortion Detection" filed June 21, 2000, which is incorporated herein by reference;

~~Attorney docket No: 0934~~ U.S. Patent Application Serial No. 09/572,860 entitled "Constellation Generation and Re-evaluation" filed May 18, 2000, now issued as U.S. Patent No. 6,721,357 which is incorporated by reference, which is incorporated herein by refererence; and

~~Attorney docket No: 0935~~ U.S. Patent Application Serial No. 09/579,529 entitled "Receiver Codec Super Set Constellation Generator" filed May 26, 2000, now issued as U.S. Patent No. 6,721,363 which is incorporated herein by refererence

Please delete the paragraph at page 35, lines 4-15, and insert the following in place thereof:

Some mu-law ~~codecs~~ CODECs violate the G. 711 ~~spec~~ specification in the RBS slot by sending the transmit encode level instead of the receive decode level for a PCM code at its decoder output. These ~~are~~ include the D4 channel bank CODECs specified in AT&T Technical Reference, PUB 43801, November 1982. These RBS slots are sometimes referred to as ½ RBS slots. Detecting this further refines the ~~codec~~ CODEC decision. This is accomplished by measuring the slot errors using the equation of Figure 10 at the test fraction equivalent to the final pad decision. The slot with this anomaly will have a ~~maxima~~ maximum at the correct pad decision instead of a ~~minima~~ minimum like all other slots. The error surface of this slot will look like Figure 12, only upside down.